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7590 04/16/2003

Steven M. Mills
MILLS & ONELLO LLP
Eleven Beacon Street, Suite 605
Boston, MA 02108

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Vanessa Marakas	(Depositor's name)
<i>Vanessa Marakas</i>	(Signature)
6-16-03	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/038,299	01/04/2002	Sang-Young Kim	SAM-0250	8434

TITLE OF INVENTION: PHASE LOCKED LOOP CIRCUIT FOR A FRACTIONAL-N FREQUENCY SYNTHESIZER

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1300	\$300	\$1600	07/16/2003

EXAMINER	ART UNIT	CLASS-SUBCLASS
CHOE, HENRY	2817	331-00100A

- | | | |
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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Samsung Electronics Co., Ltd. Republic of Korea

Please check the appropriate assignee category or categories (will not be printed on the patent) individual corporation or other private group entity government

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(Authorized Signature) *[Signature]* (Date) Reg No. 38,572 *June 16, 2003*

06/19/2003 GSEBREG1 00000024 10038299

01 FC:1501	1300.00 OP
02 FC:8001	30.00 OP
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